

J1046 U.S. PRO  
10/016449



12/10/01

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

Best Available Copy

APPL NUM 10016449	FILING DATE 12/10/2001	CLASS 714	SUBCLASS 725	GAU 2133	EXAMINER <i>Jabone</i>
<b>**APPLICANTS:</b> Bailis Robert; Kuhlmann Charles; Lingafelt Charles; Rincon Ann; <i>RKD</i>					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO RPS920010127US1	
<b>TITLE :</b> Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within th <small>U.S. DEPT. OF COMM./PAT. &amp; TM-PTO-426L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Primary Examiner	
		Application Examiner	
<input type="checkbox"/> <b>PREPARED FOR ISSUE</b>		<b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)